

A Critical Review on Mean Shift Algorithm Based Object Tracking System and its Hardware Implementation

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Abstract- Various attempts have been made to implement human vision in real-time computer vision. In most cases, however, this capability has been found to be too complex to implement on a practical systems. In short, human vision capability looks like a simple system but in reality it is difficult to model. One of the difficulties is the computational complexity required to process large amounts of data in real-time, and consequently, the design of real-time object tracking using vision remains a challenging problem. However, in this paper, we propose and implement hardware architecture for real-time object tracking system. We have chosen Mean-Shift algorithm for implementation. We will implement firstly this algorithm on MATLAB and simulated in different environments to verify the performance. After approving its robustness we implemented the algorithm on hardware.

Keywords - object tracking; Mean-Shift Algorithm; FPGA;

I. INTRODUCTION

Computer vision can be defined as a methodology that provides vision capabilities to a machine. Various attempts are being made in the computer vision industry to model human vision and to apply the same techniques to machine vision. In most of these cases researchers have had difficulty with the complexity of implementing such a capability on practical systems. These implementation problems are due to various constraints, including the lack of needed computational capability to process large amounts of data in a real-time, object motion, noise, complex background and the inability to model human vision exactly. Real-time object tracking remains a challenging problem in the computer vision industry and hence there is a huge interest on the market to make technical equipment “smart” and “self learning”[12].

Research Objective

The objectives of the current dissertation research are

1. Verification of tracking results by software implementation of Mean-Shift Algorithm on MATLAB.
2. To propose a novel hardware Architecture for Mean-Shift algorithm. The algorithm should be easy for hardware implementation.

To confirm experimentally soundness of the proposed hardware Architecture implemented through Hardware Description Language.

II. MEAN-SHIFT ALGORITHM

We assume in the sequel the support of two modules which should provide (a) detection and localization in the initial frame of the objects to track targets [5] and (b) periodic analysis of each object to account for possible updates of the target models due to significant changes in color.

Color Representation

Target Model :- Let $\{x_i^*\}_{i=1\dots n}$ be the pixel

locations of the target model, centered at zero. We define a function $b:R^2 \rightarrow \{1\dots m\}$ which associates to the pixel at location x_i^* the index $b(x_i^*)$ of the histogram bin corresponding to the color of that pixel. The probability of the color u in the target model is derived by employing a convex and monotonic decreasing kernel profile k which assigns a smaller weight to the locations that are farther from the center of the target. The weighting increases the robustness of the estimation, since the peripheral pixels are the least reliable, being often affected by occlusions (clutter) or background. The radius of the kernel profile is taken equal to one, by assuming that the generic coordinates x and y are normalized with h_x and h_y respectively. Hence, we can write [2]

$$\hat{q}_u = C \sum_{i=1}^n k \left(\|x_i^*\|^2 \right) \delta [b(x_i^*) - u] \dots\dots(1)$$

Where δ is Kronecker delta function.

$$\delta(a) = \begin{cases} 1 & \text{if } a=0 \\ 0 & \text{otherwise} \end{cases} \dots\dots(2)$$

The normalization constant C is derived by imposing the condition $\sum_{u=1}^m \hat{q}_u = 1$, from where

$$C = \frac{1}{\sum_{i=1}^n k\left(\|x_i^*\|^2\right)}, \dots\dots(3)$$

Science the summation of delta functions for $u=1\dots m$ is equal to one.

Target Candidates: Let be the pixel $\{x_i\}_{i=1\dots n_h}$

locations of the target candidate, centered at y in the current frame. Using the same kernel profile k , but with radius h , the probability of the color u in the target candidate is given by

$$\hat{p}_u(y) = C_h \sum_{i=1}^{n_h} k\left(\left\|\frac{y-x_i}{h}\right\|^2\right) \delta[b(x_i)-u] \dots\dots(4)$$

Where C_h is the normalization constant. The radius of the kernel profile determines the number of pixels (i.e., the scale) of the target candidate. By imposing the condition that $\sum_{u=1}^m \hat{p}_u = 1$ we obtain

$$C_h = \frac{1}{\sum_{i=1}^{n_h} k\left\{\left\|\frac{y-x_i}{h}\right\|^2\right\}}, \dots\dots(5)$$

Note that C_h does not depend on y , since the pixel location x_i is organized in a regular lattice, y being one of the lattice nodes. Therefore, C_h can be precalculated for a given kernel and different values of h . [12]

Kernel

Flat Kernel

$$k(x) = \begin{cases} 1 & \text{if } \|x\| \leq 1 \\ 0 & \text{otherwise} \end{cases} \dots\dots(6)$$

Gaussian Kernel

$$k(x) = \exp\left(-\|x\|^2\right) \dots\dots(7)$$

Epanechnikov Kernel

$$k(x) = \begin{cases} \frac{1}{2} c_d^{-1} (d+2)(1-x) & \text{if } x \leq 1 \\ 0 & \text{otherwise} \end{cases} \dots\dots(8)$$

Where d is the number of dimension and C_d is the area of a unit circle in d dimension. [5]

III. LITERATURE REVIEW

Su Liu, Alexandros Papakonstantinou, Hongjun Wang, Deming Chen [1] said Object tracking is an important task in computer vision applications. One of the crucial challenges is the realtime speed requirement. In this paper we implement an object tracking system in reconfigurable hardware using an efficient parallel architecture. In our implementation, we adopt a background subtraction based algorithm. The designed objecttracker exploits hardware parallelism to achieve high system speed. We also propose a dual object region search technique to further boost the performance of our system under complex tracking conditions. For our hardware implementation we use the Altera Stratix III EP3SL340H1152C2 FPGA device. We compare the proposed FPGA based implementation with the software implementation running on a 2.2 GHz processor. The observed speedup can reach more than 100X for complex video.

Zhi-Qiang Wen and Zi-Xing Cai [12] applied mean shift algorithm with Gaussian profile for tracking of objects. The imprecise proofs about convergence of mean shift were firstly pointed out. Then a convergence theorem and its rigorous convergence proof were provided. Lastly tracking approach of objects based on mean shift was modified. The results of experiment showed the modified approach had good performance of object tracking applied to occlusion.

Dorin Comaniciu, Visvanathan Ramesh and Peter Meer [5] regularized the feature histogram-based target representations by spatial masking with an isotropic kernel. The masking induced spatially-smooth similarity functions suitable for gradient-based optimization; hence, the target localization problem can be formulated using the basin of attraction of the local maxima. They employed a metric derived from the Bhattacharyya coefficient as similarity measure, and used the mean shift procedure to perform the optimization. According to the algorithm for a given target model, the location of the target in the current frame minimizes the distance in the neighborhood of the previous location estimate. However, the scale of the target often changes in time. They employed a metric derived from the Bhattacharyya coefficient as similarity measure, and used the mean shift procedure to perform the optimization.

Usman Ali, M. B. Malik and K. Munawar [5] presented an architecture based on soft RISC processor for kernel based mean shift algorithm in 2009. Design was based on Xilinx Micro blaze Soft processor core. Video decoder, Frame grabber, Video display unit and Video multiplexer modules were implemented in Xilinx FPGA tool (ISE). The complete system which included soft processor, multi-channel memory controller with DDR RAM interface and all other OPB peripherals were implemented in Xilinx EDK tool.

MATLAB Implementation:

We have taken several different videos with different resolutions and different environmental condition. We have applied the algorithm and results are shown below. We have compared every video taking two bin sizes 16 and 8 and also calculated false detection rate and frame rate

Helicopter Sequence
Resolution = 320*240
Number of bins = 16



Figure 1 Helicopter sequence for 16 bins

HDL Implementation

Top Level Architecture for Object Tracking

The Object Tracking deals with tracking of objects which are of interest. The algorithm followed for implementation of tracking is Mean-shift. Mean-shift uses Histogram and Bhattacharyya coefficient for tracking the position of object in subsequent frames of videos by taking the fact into consideration that object will not displace much in continuous frames.

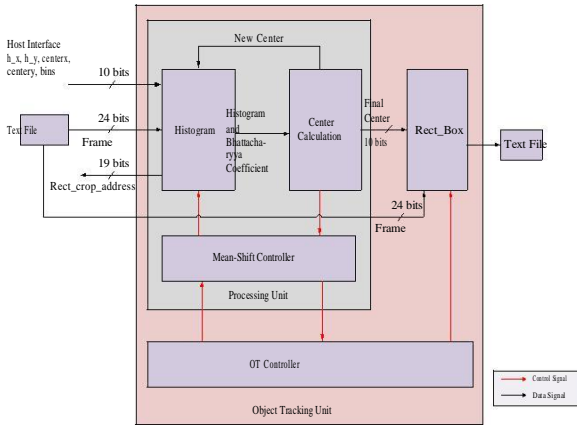


Figure 2 Mean-shift algorithms for Object Tracking [5]

The above is the top level architecture which we have proposed for the hardware implementation of Mean-shift algorithm for Object Tracking. To provide a RGB image as an input to this architecture we have converted an image into a text file representing R,G and B values of each pixel in hexadecimal format[5]. Each pixel is read by histogram block as a frame. A frame is a 24-bit wide R,G,B values, 8-bits each, of a pixel representing intensity values of a pixel of a colored image. Host interfaces are inputs given by user in order to specify the object to be tracked in an image

Histogram

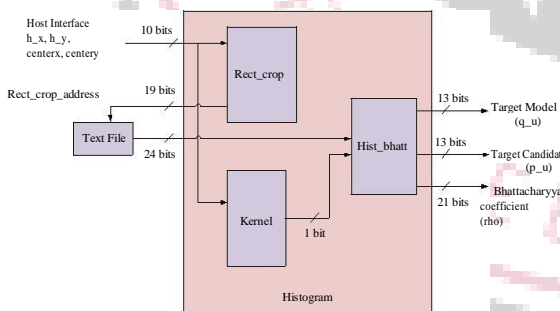


Figure 3 Histogram [5]

Sub modules of Histogram module are – Rect_crop, Kernel, Hist_bhattach. Rect_crop sub module is responsible for generating address of pixels of object of interest using (5.1).

As we have explained early that for processing of algorithm we do not require complete image, rather we just want only object of interest. So we need to crop our image and extract object of interest. For that we have designed an equation which calculates addresses of only those pixels related to crop image. [5]

Where x_1, x_2, y_1 and y_2 are corner pixel location of cropped image. When Rect_crop calculates address of the desired pixel at the same time kernel sub module calculates value of the kernel for the same pixel. For our implementation we have taken Flat Kernel (3.6). Flat

Kernel is either „0“ or „1“ so only 1-bit is required. In Hist_bhattach sub module histogram for the desired object and Bhattacharyya Coefficient are calculated. Histogram is stored in histogram buffer. Size of the histogram buffer depends on the number of bins provided via host interface input. In our case we have taken it 8 bins, so the size of the histogram buffer would be $bins * bins * bins = 512$ locations. When pixel intensity value and kernel value reaches to the Hist_bhattach sub module, address for the histogram buffer can be calculated by pixel intensity value. In order to calculate histogram kernel generated is added to the location addressed by Hsit_bhattach sub module in histogram buffer.

Center Calculation

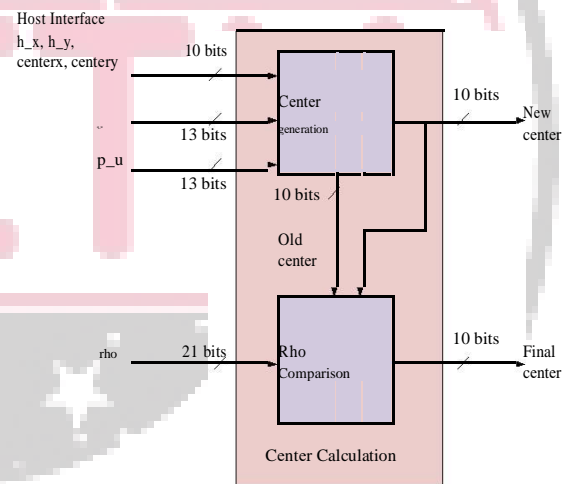


Figure 4 Center Calculation [5]

After the calculation of Target Model and Target Candidate a signal named hist_done enables Center Generation sub module. This sub module is the heart of the Object Tracking Architecture because it predicts the next possible center which is stored as new_center. In Center Generation sub module weight for each pixel of Object of Interest is calculated. Then every weight is multiplied with normalized pixel location in order to predict new_center.

Rho Comparison is the sub module in which Bhattacharyya coefficient calculated by Histogram module at `old_center` and `new_center` is compared. Comparison tells us which center is the correct center and it also tells us that shall we go for iteration loop or not. So from the Rho Comparison sub module, exact center is finalized which is stored as `final_center` and given to the `Rect_box` module to draw the rectangular box around tracked moving object. Then `Rect_box` module writes the image pixel values into a output text file

IV. CONCLUSION

The objective of this paper was to review the hardware design for real-time object tracking. Several algorithms for detection and tracking of moving objects have been proposed worldwide. Most of the researchers implemented software model of all available algorithms but only few of them attempted for their hardware model. Inspiring by wide range of application and excellent performance results presented by other researches we have chosen Mean-Shift algorithm for tracking. In first phase we implemented software model. We verified the robustness, capability of tracking object under partial occlusion, intensity variation and in random environment. In second phase we proposed hardware architecture for the design and implemented hardware model using HDL

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